



RECEIVED

OCT 02 2001

Technology Center 2100
Page 1 of 1

U.S. DEPT. OF COMMERCE PATENT AND TRADEMARK OFFICE

Form PTO-1449		ATTORNEY DOCKET NO. AUS920010565US1		SERIAL NO. 09/925,584	
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)					
APPLICANT Austen et al.					
FILING DATE August 9, 2001				GROUP ART UNIT 2182	
U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/SUBCLASS	FILING DATE
SB	AA 4564903	Jan. 14, 1986	Guyette et al.	364/300	Oct. 5, 1983
SB	AB 4843541	Jun. 27, 1989	Bean et al.	364/200	Jul. 29, 1987
SB	AC 5345590	Sep. 6, 1994	Ault et al.	395/650	Sep. 1, 1993
FOREIGN PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION YES NO
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)					
SB	AD	IBM Technical Disclosure Bulletin, Vol. 39, No. 12, December 1996, "Hypervisor High Performance Synchronous Dispatch for Coupled Systems", one page.			
SB	AE	IBM Technical Disclosure Bulletin, Vol. 38, No. 04, April 1995, "VM MPG Operating as a DRF Hypervisor as a First Level Guest Under PR/SM", p. 325.			
SB	AF	IBM Technical Disclosure Bulletin, Vol. 36, No. 03, March 1993, "Sharing Read-Only Memory among Multiple Logical Partitions", pp. 303-304.			
SB	AG	IBM Technical Disclosure Bulletin, Vol. 39, No. 12, December 1996, "Highly Parallel Coupling Facility Emulator/Router with Shadowed Link Buffers", 2 pages.			
SB	AH	IBM Technical Disclosure Bulletin, Vol. 39, No. 06, June 1996, "Coordinating Multiple Server Partitions to Enter Power-Save State", pp. 235-239.			
DATE CONSIDERED 11/9/04		EXAMINER Sureth R Suryawanshi			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					